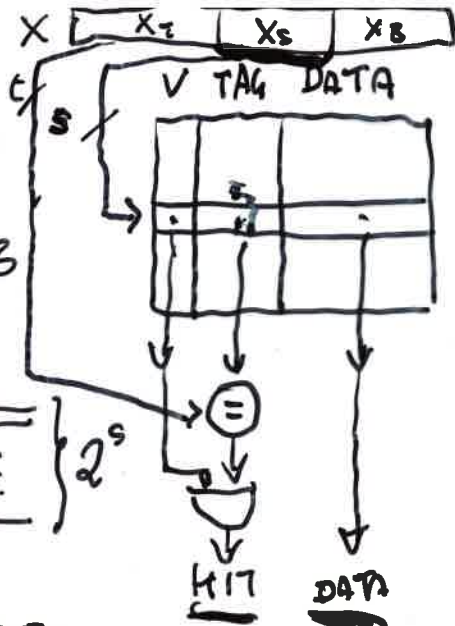


$C = 64B$
 $A = \cancel{X}1$
 $B = 32B$



X_B INDIRIZZO AL BYTE
 ALL'INTERNO DEL BLOCCO

X_S INDIRIZZO DEL BLOCCO ("SET")

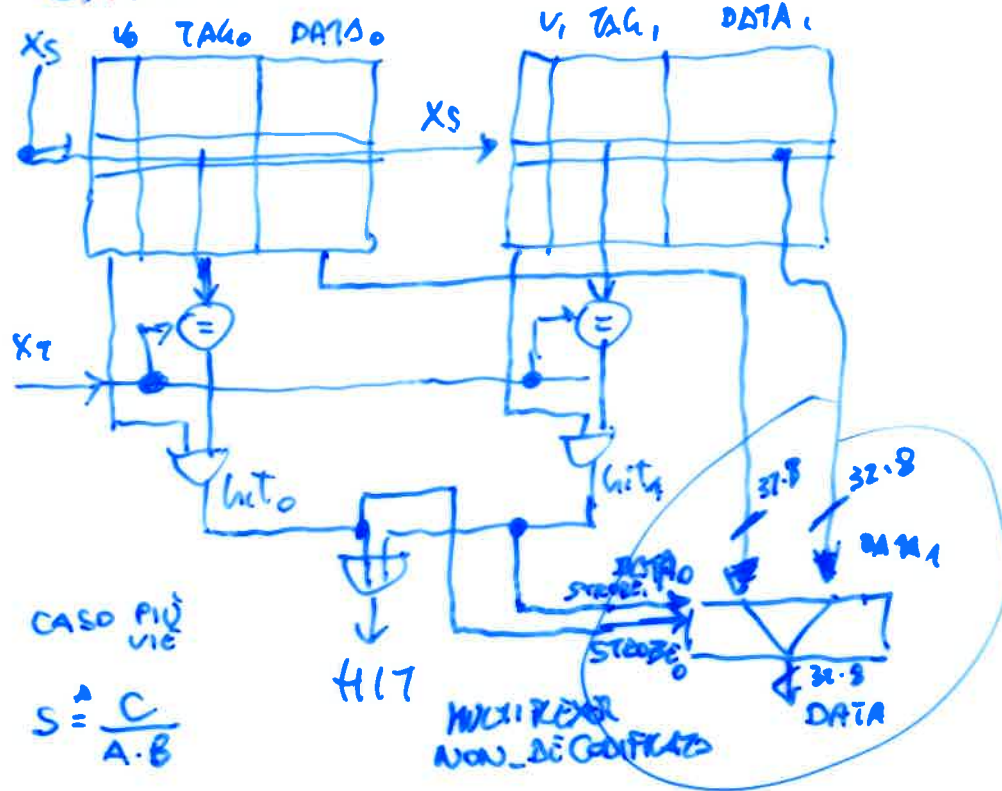
X_T RESTANTI BIT USATI TAG

FACCIO IN MODO CHE

X_S POSSA INDIRIZZARE TUTTI I DATI
 IN CACHE

CACHE AD
 ACCESSO DIRETTO
 (1 VIA)

CACHE A 2 VIE



$X_M = \text{IND. BLOCCO IN MEM.} = X/B$
 $X_T = \text{PARTE TAG DELL'IND.} = X_M/S \quad S = \frac{C}{B}$
 $X_S = \text{IND. SET (BLOCCO)} = X_M \bmod S$
 $(X_B = \text{NO. BYTE NEL BLOCCO})$

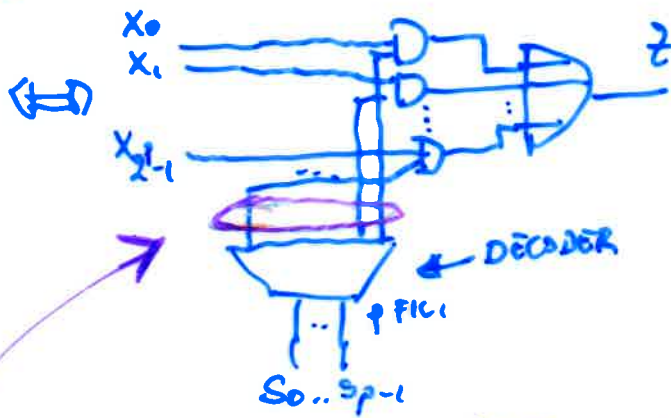
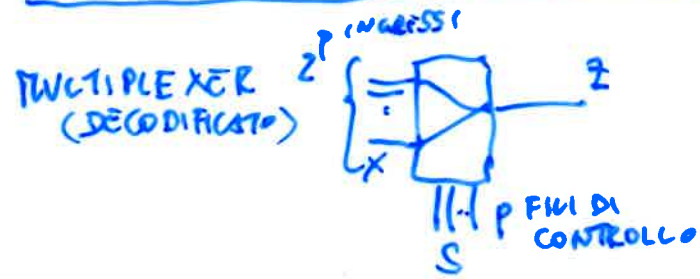
CASO PIU' VIE

$$S = \frac{C}{A \cdot B}$$

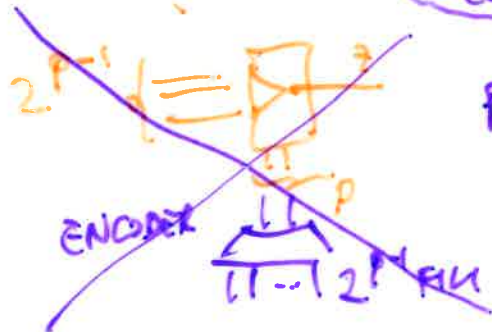
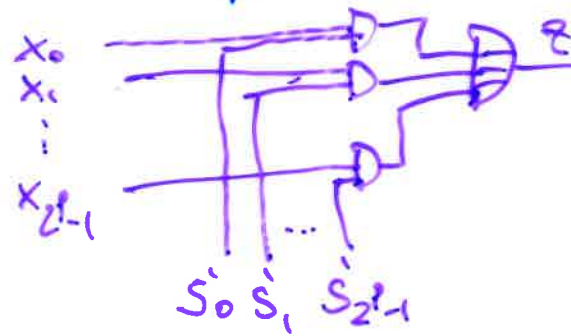
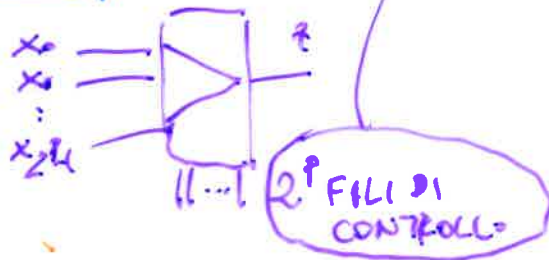
HIT

MULTIPLEXER NON DECODIFICATO

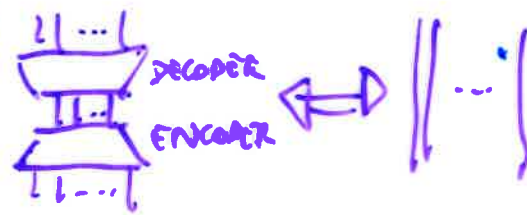
MULTIPLEXER NON DECODIFICATO



MULTIPLEXER
(NON DECODIFICATO)



PER CIRCUITI



$$\left. \begin{array}{l} C = 64 \text{ B} \\ A = 2 \\ B = 32 \end{array} \right\} S = \frac{C}{A \cdot B} = \frac{64}{2 \cdot 32} = 1$$



$$t_{hit} = 5 \text{ ns}$$

$$t_{pen} = 60 \text{ ns}$$

$P_{RIMP.} = LRU \Rightarrow$ LEAST RECENTLY USED
 ↳ IN CASO DI RIMPIAZZAMENTO SCARTO IL BLOCCO PIÙ USATO

X	X _M	X _T	X _S	H/M	LRU ₀	LRU ₁	
21	0	0	0	0	0	0	
77	2	2	0	0	0	2	
66	2	2	0	0	0	2	RIMPIAZZAMENTO
146	4	4	0	0	1	2	→ X _M =0, X _T =0
82	2	2	0	1	0	2	LRU=0,1
15	0	0	0	0	0	4	→ X _M =4, X _T =4
130	4	4	0	0	0	4	→ X _M =2, X _T =2
64	2	2	0	0	0	4	→ X _M =0, X _T =0
192	6	6	0	0	0	6	→ X _M =4, X _T =4
209	6	6	0	1	0	6	
225	7	7	0	0	0	6	→ X _M =2, X _T =2
241	7	7	0	1	0	6	
113	3	3	0	0	0	3	→ X _M =6, X _T =6
97	3	3	0	1	0	3	
223	8	8	0	0	0	3	→ X _M =7, X _T =7
66	2	2	0	0	0	2	→ X _M =3, X _T =3
257	8	8	0	1	0	2	
129	4	4	0	0	0	4	→ X _M =2, X _T =2
64	2	2	0	0	0	4	→ X _M =8, X _T =8
161	5	5	0	0	0	5	→ X _M =4, X _T =4
240	7	7	0	0	0	7	→ X _M =2, X _T =2
299	9	9	0	0	0	7	→ X _M =5, X _T =5

$$\Delta_{MAT} = t_{hit} + m \cdot t_{pen} = 5 + \frac{16}{22} \cdot 60 \approx 48.6 \text{ ns}$$

DOMANDE (i)

AG ALTERNING

[7 | 9] (ii)

VIA0 VIA1